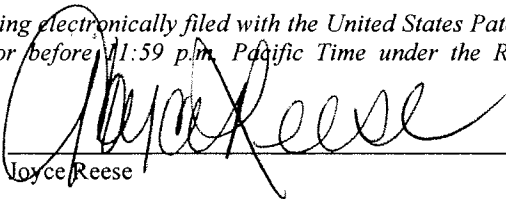


PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

*I hereby certify that this correspondence is being electronically filed with the United States Patent and Trademark Office on October 28, 2008 at or before 11:59 p.m. Pacific Time under the Rules of 37 CFR § 1.8.*

  
Joyce Reese

Appl No. : 10/769,488  
Applicant : Imada, Shougo  
Filed : January 30, 2004  
Title : MICROCOMPUTER LOGIC DEVELOPMENT

Confirmation No. 8744

TC/A.U. : 2123  
Examiner : Dwain M. Craig

Docket No. : 51879/A400  
Customer No. : 23363

**AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
October 28, 2008

Commissioner:

In response to the Office action of May 28, 2008, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 10 of this paper.